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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Kenji YAMAUCHI

Serial No.: 09/733,036 Group Art Unit: 2828

Filed: December 11, 2000 Examiner: Leung, Quyen Phan

For: SEMICONDUCTOR LASER DIODE CHIP AND ITS POSITIONING  
AND MOUNTING METHOD

Honorable Commissioner of Patents  
Alexandria, Virginia 22313-1450

**STATEMENT OF SUBSTANCE OF INTERVIEW**

Sir:

Applicant thanks the Examiner for the telephone conversation of April 5, 2004.

As stated in the Interview Summary of Examiner Leung, dated April 5, 2004, the Examiner stated that the case would be allowed if claims 29-30 were canceled. The Applicant agreed.

Respectfully submitted,

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Date: May 5, 2004  
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